



<b>UTILITY PATENT APPLICATION TRANSMITTAL</b> <small>(Only for new non-provisional applications under 37 CFR 1.53(b))</small>		Attorney Docket No. <b>210067US2</b>
First Inventor or Application Identifier <b>Hidemasa ZAMA, et al.</b>		Title <b>SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLIP FLOP</b>
Assignee Name: <b>KABUSHIKI KAISHA TOSHIBA</b> Assignee Address: <b>72, HORIKAWA-CHO, SAIWAI-KU, KAWASAKI-SHI, KANAGAWA-KEN, JAPAN</b>		1103-3 U.S. PTO 01/883959 06/20/01

<b>APPLICATION ELEMENTS</b> See MPEP chapter 600 concerning utility patent application contents		<b>ADDRESS TO:</b> Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
<b>1. Fee Transmittal Form (e.g. PTO/SB/17)</b> (Submit an original and a duplicate for fee processing)		<b>ACCOMPANYING APPLICATION PARTS</b> 7. <input type="checkbox"/> Assignment Papers (cover sheet & document(s)) 8. <input checked="" type="checkbox"/> Application Data Sheet. See 37 CFR 1.76 9. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney (when there is an assignee) 10. <input type="checkbox"/> English Translation Document (if applicable) 11. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations 12. <input type="checkbox"/> Preliminary Amendment 13. <input checked="" type="checkbox"/> White Advance Serial No. Postcard 14. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) (1) (if foreign priority is claimed) 15. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27 16. <input checked="" type="checkbox"/> Other: Notice of Priority, List of Inventors' Names and Addresses
<b>2. Specification</b> Total Sheets <b>19</b>		
<b>3. Drawing(s) (35 U.S.C. 113)</b> Total Sheets <b>6</b> (Formals)		
<b>4. Oath or Declaration</b> Total Pages <input type="text"/>		
a. <input type="checkbox"/> Newly executed (original or copy) b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation / divisional w/ box 17 completed) i. <input type="checkbox"/> <b>DELETION OF INVENTOR(S)</b> Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).		
<b>5. CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)</b>		
<b>6. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)</b> a. <input type="checkbox"/> Computer Readable Form (CRF) b. Specification or Sequence Listing on: i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); or ii. <input type="checkbox"/> Paper c. <input type="checkbox"/> Statements verifying identity of above copies		
<b>7. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:</b> <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.: Prior application information: Examiner: Group Art Unit:		
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation <u>can only</u> be relied upon when a portion has been inadvertently omitted from the submitted application parts.		
<b>18. Amend the specification by inserting before the first line the sentence:</b> <input type="checkbox"/> This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on <input type="checkbox"/> Which was published in English <input type="checkbox"/> Which was not published in English <input type="checkbox"/> This application claims priority of provisional application Serial No. Filed		
<b>19. CORRESPONDENCE ADDRESS</b>  <b>22850</b> (703) 413-3000 FACSIMILE: (703) 413-2220		

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hidemasa ZAMA, et al.

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLIP FLOP

## FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	17 - 20 =	0	× \$18 =	\$0.00
INDEPENDENT CLAIMS	5 - 3 =	2	× \$80 =	\$160.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$270 =	\$0.00
<input checked="" type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$130.00
			BASIC FEE	\$710.00
			TOTAL OF ABOVE CALCULATIONS	\$1,000.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
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The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030.  
A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

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